**Regarding Central\_UART**

Central\_UART using NCS SDK 2.4.0.

Board Configure：nrf52840dk\_nrf52840

Configuration：prj.conf

Extra CMake arguments：-DSHIELD=nrf21540\_ek

#-----------------------------------------------------------------------------------------

// Add the following configuration to nrf52840dk\_nrf52840.overlay.

// Add the following configuration to app.overlay.

/ {

    aliases {

            nrf21540-spi = &spi3;

    };

};

/{

nrf\_radio\_fem: nrf21540\_fem {

    compatible = "nordic,nrf21540-fem";

    tx-en-gpios = <&gpio0 22 GPIO\_ACTIVE\_HIGH>;

    rx-en-gpios = <&gpio0 19 GPIO\_ACTIVE\_HIGH>;

    pdn-gpios = <&gpio0 23 GPIO\_ACTIVE\_HIGH>;

    //ant-sel-gpios = <&gpio0 20 GPIO\_ACTIVE\_HIGH>;

    mode-gpios = <&gpio1 7 GPIO\_ACTIVE\_HIGH>;

    spi-if = <&nrf\_radio\_fem\_spi>;

    supply-voltage-mv = <3000>;

    };

};

fem\_spi: &spi3 {

    status = "okay";

    pinctrl-0 = <&spi3\_default\_alt>;

    pinctrl-1 = <&spi3\_sleep\_alt>;

    pinctrl-names = "default", "sleep";

    cs-gpios = <&gpio0 21 GPIO\_ACTIVE\_LOW>;

    nrf\_radio\_fem\_spi: nrf21540\_fem\_spi@0 {

            compatible = "nordic,nrf21540-fem-spi";

            status = "okay";

            reg = <0>;

            spi-max-frequency = <8000000>;

    };

};

&pinctrl {

    uart0\_default: uart0\_default {

        group1 {

            psels = <NRF\_PSEL(UART\_TX, 1, 2)>;

        };

        group2 {

            psels = <NRF\_PSEL(UART\_RX, 1, 1)>;

            bias-pull-up;

        };

    };

    uart0\_sleep: uart0\_sleep {

        group1 {

            psels = <NRF\_PSEL(UART\_TX, 1, 2)>,

                <NRF\_PSEL(UART\_RX, 1, 1)>;

            low-power-enable;

        };

    };

    spi3\_default\_alt: spi0\_default\_alt {

        group1 {

            psels = <NRF\_PSEL(SPIM\_SCK, 1, 6)>,

                <NRF\_PSEL(SPIM\_MOSI, 1, 8)>,

                <NRF\_PSEL(SPIM\_MISO, 1, 4)>;

        };

    };

    spi3\_sleep\_alt: spi0\_sleep\_alt {

        group1 {

            psels = <NRF\_PSEL(SPIM\_SCK, 1, 6)>,

                <NRF\_PSEL(SPIM\_MOSI, 1, 8)>,

                <NRF\_PSEL(SPIM\_MISO, 1, 4)>;

            low-power-enable;

        };

    };

};

&qspi {

    status = "disabled";

};

&uart1 {

    status = "disabled";

};

 &uart0 {

    status = "okay";

    current-speed = <115200>;

    pinctrl-0 = <&uart0\_default>;

    pinctrl-1 = <&uart0\_sleep>;

    pinctrl-names = "default", "sleep";

};

/ {

    chosen {

        nordic,nus-uart = &uart0;

    };

};

#-----------------------------------------------------------------------------------------

// Add the following configuration to prj.conf.

. . . . .

CONFIG\_MPSL=y

CONFIG\_MPSL\_FEM=y

CONFIG\_MPSL\_FEM\_NRF21540\_GPIO=y

CONFIG\_MPSL\_FEM\_NRF21540\_GPIO\_SPI=n

CONFIG\_MPSL\_FEM\_NRF21540\_TX\_GAIN\_DB=10

CONFIG\_MPSL\_FEM\_NRF21540\_TX\_GAIN\_DB\_POUTA=10

CONFIG\_MPSL\_FEM\_NRF21540\_TX\_GAIN\_DB\_POUTB=10

CONFIG\_BT\_CTLR\_TX\_PWR\_ANTENNA=17